



PRAGATI ENGINEERING COLLEGE

1-378, ADB Road, Surampalem-533437

(Approved by AICTE, Permanently Affiliated to JNTUK, KAKINADA)

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLES 2020-21

ACADEMIC YEAR 2020-21				II ECE 'A' I SEM			
Days	01.00-2.00 PM	2.00-3.00PM	3.00-4.00PM	4.00-5.00 PM	5.00-6.00 PM	6.00-7.00 PM	7.00-8.00 PM
	1	2	3	4	5	6	7
MON	S&S	IoT	CS	EITK	BREAK		
TUE	IoT	EDC LAB			BREAK		
WED	EITK	STLD	S&S	MEFA	BREAK	BEEE/STLD LAB	
THU	STLD	IoT	MEFA	S&S	BREAK		
FRI	MEFA	STLD	CS		BREAK		
SAT	CS	BEEE/STLD LAB			BREAK		
IoT	Internet of Things				Mr JOHN SAIDA SHAIK		
S&S	Signals & Systems				Dr.SANJIT KUMAR DASH		
CS	Control Systems				Mr.G S SIVAKUMAR		
STLD	Switching Theory & Logic Design				Mrs.K.DEVI SRI SNIGDHA		
EITK	Essence of Indian Traditional Knowledge				Mr.J.D.NAIDU		
MEFA	Managerial Economics and Financial Analysis				Mr.B.RAJESH KUMAR		
EDC LAB	Electronic Devices and Circuits Lab				Dr.SANJIT KUMAR DASH Mr. T VISHNU MURTHY Ms.RUBIA TASNEEM Mrs.P RAMYA KRISHNA Mr P KRISHNA CHAITANYA		
STLD LAB	Switching Theory & Logic Design Lab				Mr.K RAGHURAM Mrs.K.DEVI SRI SNIGDHA Mr.G.RAJU		
BEEE LAB	Basic Electrical & Electronics Engineering Lab						



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ACADEMIC YEAR 2020-21				II ECE 'B' I SEM			
Days	09-00 - 10.00 am.	10.00 - 11.00 am.	11.00 - 12.00 pm.	12.00-1.00 pm	1.00- 2.00 pm	2.00- 3.00 pm	3.00- 4.00pm
	1	2	3	4	5	6	7
MON	IoT	EDC LAB			LUNCH		
TUE	S&S	CS	IoT	LUNCH	BEEE/STLD LAB		
WED	CS	EITK	S&S	LUNCH	STLD		
THU	EITK	BEEE/STLD LAB			LUNCH		
FRI	STLD	MEFA	IoT	LUNCH	MEFA		
SAT	MEFA	CS	S&S	LUNCH	STLD		
IoT	Internet of Things				Dr.SUBODH KUMAR PANDA		
S&S	Signals & Systems				Dr.SAILAJA VEMURI		
CS	Control Systems				Mrs.P RAMYA KRISHNA		
STLD	Switching Theory & Logic Design				Mr.K RAGHURAM		
EITK	Essence of Indian Traditional Knowledge				Mr.S.VAMSI KIRAN		
MEFA	Managerial Economics and Financial Analysis				Dr.K.CHAKRAPANI		
EDC LAB	Electronic Devices and Circuits Lab				Dr.SAILAJA VEMURI Dr.SANJIT KUMAR DASH Mr. T VISHNU MURTHY Mr.K T K MOHAN Mrs B VASANTHA LAKSHMI		
STLD LAB	Switching Theory & Logic Design Lab				Mrs.K.DEVI SRI SNIGDHA Mrs K SURYA KUMARI Mr.G.RAJU		
BEEE LAB	Basic Electrical & Electronics Engineering Lab						



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ACADEMIC YEAR 2020-21		II ECE 'C' I SEM					
Days	09-00 - 10.00 am.	10.00 - 11.00 am.	11.00 - 12.00 pm.	12.00-1.00 pm	1.00- 2.00 pm	2.00- 3.00 pm	3.00-4.00pm
	1	2	3	4	5	6	7
MON	MEFA	IoT	STLD	LUNCH	EITK		
TUE	CS	S&S	MEFA	LUNCH	IoT		
WED	S&S	BEEE/STLD LAB			LUNCH		
THU	STLD	EDC LAB			LUNCH		
FRI	IoT	BEEE/STLD LAB			LUNCH		
SAT	EITK	CS	STLD	LUNCH	CS	MEFA	S&S
IoT	Internet of Things				Ms.RUBIA TASNEEM		
S&S	Signals & Systems				Mr.V SATTI BABU		
CS	Control Systems				Mrs.P RAMYA KRISHNA		
STLD	Switching Theory & Logic Design				Mr.K RAGHURAM		
EITK	Essence of Indian Traditional Knowledge				Mr.K.BALAJI		
MEFA	Managerial Economics and Financial Analysis				Mr.B.RAJESH KUMAR		
EDC LAB	Electronic Devices and Circuits Lab			Ms.RUBIA TASNEEM Dr.SAILAJA VEMURI Mrs B VASANTHA LAKSHMI Mr. T VISHNU MURTHY Mrs.P RAMYA KRISHNA			
STLD LAB	Switching Theory & Logic Design Lab			Mr.K RAGHURAM Mr.G.RAJU Mr.K.AKHIL			
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	1	2	3	4	5	6	7
MON	MEFA	BEEE/STLD LAB			LUNCH		
TUE	EITK	IoT	STLD	LUNCH	S&S		
WED	CS	EDC LAB			LUNCH		
THU	EITK	S&S	IoT	STLD	LUNCH		
FRI	STLD	CS	MEFA	LUNCH	BEEE/STLD LAB		
SAT	MEFA	S&S	IoT	LUNCH	CS		
IoT	Internet of Things				Ms.RUBIA TASNEEM		
S&S	Signals & Systems				Dr.SANJIT KUMAR DASH		
CS	Control Systems				Mrs K DEEPTHI		
STLD	Switching Theory & Logic Design				Mrs.K.DEVI SRI SNIGDHA		
EITK	Essence of Indian Traditional Knowledge				Mr.S.PRASAD		
MEFA	Managerial Economics and Financial Analysis				Mr.K.ANAND		
EDC LAB	Electronic Devices and Circuits Lab				Dr.G RAGHURAM Ms.RUBIA TASNEEM Dr.SAILAJA VEMURI Mrs B VASANTHA LAKSHMI Mrs.P RAMYA KRISHNA		
STLD LAB	Switching Theory & Logic Design Lab				Mr M.BRAHMARAJU Mr.K.AKHIL Mr.G.RAJU		
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ACADEMIC YEAR 2020-21		III ECE 'A' I SEM					
Days	01.00-2.00 PM	2.00-3.00PM	3.00-4.00PM	4.00-5.00 PM	5.00-6.00 PM	6.00-7.00 PM	7.00-8.00 PM
	1	2	3	4	5	6	7
MON	LICA	DICA & DSD (T)	AP	DC	BREAK	DC	
TUE	DICA	DICA	AP (T)	BREAK	IC PDC/DCLAB		
WED	CAO	DC	LICA (T)	BREAK	DICA LAB		
THU	DC (T)	IC PDC/DCLAB			LUNCH	PEHV	CAO
FRI	AP	DICA	LICA	BREAK	CAO (T)	LICA	
SAT	PEHV	PEHV	CAO	AP	BREAK		
CAO		Computer Architecture & Organization			Mr.Y.PAVAN KUMAR		
DC		Digital Communications			Mr.B SUDHIR		
AWP		Antennas and Wave Propagation			Mr CH VENKATESWARLU		
LICA		Linear IC Applications			Dr.D.B V N S M NAGESH		
DICA & DSD		Digital System Design & Digital IC Applications			Mrs A S AMRUTHAVARSHINI		
PEHV		Professional Ethics and Human Values			Mrs.P.SRAVANI		
TUTORIAL HOURS		CAO			Mr.Y.PAVAN KUMAR		
		DC			Mr.B SUDHIR		
		AWP			Mr CH VENKATESWARLU		
		LICA			Dr.D.B V N S M NAGESH		
		DICA & DSD			Mrs A S AMRUTHAVARSHINI		
DC LAB		Digital Communications Lab			Mr.B SUDHIR Mr.P VENKATESH Mrs A S AMRUTHAVARSHINI		
DICA Lab		Digital System Design & DICA Lab			Mrs.P SUNITHA Ms.G. VIRANYA Mrs G SRILAKSHMI Ms.S.VAISHNAVI		
IC-PDC LAB		Integrated Circuits & Pulse & Digital Circuits Lab			Dr.SUBODH KUMAR PANDA Mr.G S SIVAKUMAR Ms.P.VISWA SANTHI		



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	1	2	3	4	5	6	7
MON	LICA (T)	DC	AP	LUNCH	CAO (T)	DICA (T)	
TUE	AP	DICA LAB			LUNCH	LICA	
WED	CAO	CAO	LICA	LUNCH	IC PDC/DCLAB		
THU	DICA	DICA	DC	LUNCH	AP	LICA	
FRI	DC (T)	IC PDC/DCLAB			LUNCH	PEHV (T)	
SAT	PEHV	DC	AP (T)	LUNCH	CAO	DICA	PEHV
CAO	Computer Architecture & Organization				Mrs B S S VIDYA		
DC	Digital Communications				Ms.D V V LAKSHMI		
AWP	Antennas and Wave Propagation				Mr P KRISHNA CHAITANYA		
LICA	Linear IC Applications				Ms.P.VISWA SANTHI		
DICA & DSD	Digital System Design & Digital IC Applications				Mr.V PRASANTH		
PEHV	Professional Ethics and Human Values				Mrs.S.BHARGAVI		
TUTORIAL HOURS	CAO				Mrs B S S VIDYA		
	DC				Ms.D V V LAKSHMI		
	AWP				Mr P KRISHNA CHAITANYA		
	LICA				Ms.P.VISWA SANTHI		
	DICA & DSD				Mr.V PRASANTH		
DC LAB	Digital Communications Lab				Ms.D V V LAKSHMI Mr.P VENKATESH Mr K PASIPALANA RAO		
DICA Lab	Digital System Design & DICA Lab				Mrs.P SUNITHA Mr.V.PRASANTH Ms.G. VIRANYA Mrs G SRILAKSHMI		
IC-PDC LAB	Integrated Circuits & Pulse & Digital Circuits Lab				Mrs B S S VIDYA Mr.B.KALYAN CHAKRAVARTHY Ms.P.VISWA SANTHI		



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	1	2	3	4	5	6	7
MON	PEHV	IC PDC/DCLAB			LUNCH	LICA	DC (T)
TUE	AP	DC	LICA	LUNCH	CAO	CAO (T)	PEHV (T)
WED	LICA (T)	DC	DICA	PEHV	LUNCH	AP	
THU	DICA (T)	CAO	AP	LUNCH	IC PDC/DCLAB		
FRI	CAO	DICA LAB			LUNCH	DICA	
SAT	DC	DICA	LICA	LUNCH	AP (T)		
CAO	Computer Architecture & Organization				Ms.P ANURADHA		
DC	Digital Communications				Mr.P VENKATESH		
AWP	Antennas and Wave Propagation				Mr CH VENKATESWARLU		
LICA	Linear IC Applications				Mr.B.KALYAN CHAKRAVARTHY		
DICA & DSD	Digital System Design & Digital IC Applications				Dr.P SUNITHA		
PEHV	Professional Ethics and Human Values				Mrs.P.SRAVANI		
TUTORIAL HOURS	CAO				Ms.P ANURADHA		
	DC				Mr.P VENKATESH		
	AWP				Mr CH VENKATESWARLU		
	LICA				Mr.B.KALYAN CHAKRAVARTHY		
	DICA & DSD				Dr.P SUNITHA		
DC LAB	Digital Communications Lab				<div style="border: 1px solid black; padding: 5px;"> Mr.B SUDHIR Ms.CH.N.S.T.DEVI Mr.K T K MOHAN </div>		
DICA Lab	Digital System Design & DICA Lab				Mrs.P SUNITHA Mr.V.PRASANTH Mrs G SRILAKSHMI Ms.P.ANURADHA		
IC-PDC LAB	Integrated Circuits & Pulse & Digital Circuits Lab				Dr.SUBODH KUMAR PANDA Mr.G S SIVAKUMAR Ms.P.VISWA SANTHI		



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ACADEMIC YEAR 2020-21		III ECE 'D' I SEM					
Days	09-00 - 10.00 am.	10.00 - 11.00 am.	11.00 - 12.00 pm.	12.00-1.00 pm	1.00-2.00 pm	2.00-3.00 pm	3.00-4.00pm
	1	2	3	4	5	6	7
MON	DC	AP (T)	LICA (T)	LUNCH	DICA LAB		
TUE	PEHV	IC PDC/DCLAB			LUNCH	CAO	
WED	LICA	DICA	DICA	LUNCH	LICA	DC	
THU	PEHV	PEHV (T)	LICA	CAO	LUNCH	CAO (T)	
FRI	AP	AP	DC	LUNCH	IC PDC/DC LAB		
SAT	DICA	DICA (T)	CAO	LUNCH	AP	DC (T)	
CAO	Computer Architecture & Organization				Mrs B S S VIDYA		
DC	Digital Communications				Ms.D V V LAKSHMI		
AWP	Antennas and Wave Propagation				Mr P KRISHNA CHAITANYA		
LICA	Linear IC Applications				Mr.B.KALYAN CHAKRAVARTHY		
DICA & DSD	Digital System Design & Digital IC Applications				Mr.V PRASANTH		
PEHV	Professional Ethics and Human Values				Mrs.S.BHARGAVI		
TUTORIAL HOURS	CAO				Mrs B S S VIDYA		
	DC				Ms.D V V LAKSHMI		
	AWP				Mr P KRISHNA CHAITANYA		
	LICA				Mr.B.KALYAN CHAKRAVARTHY		
	DICA & DSD				Mr.V PRASANTH		
DC LAB	Digital Communications Lab				Mr.K T K MOHAN Ms.CH.N.S.T.DEVI Ms.D V V LAKSHMI		
DICA Lab	Digital System Design & DICA Lab				Mr.V.PRASANTH Ms.G. VIRANYA Mrs G SRILAKSHMI Ms.P.ANURADHA		
IC-PDC LAB	Integrated Circuits & Pulse & Digital Circuits Lab				Mrs B S S VIDYA Mr.R.V.SIMHADRI Mrs K DEEPTHI		



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Days	09-00 - 10.00 am.	10.00 - 11.00 am.	11.00 - 12.00 pm.	12.00-1.00 pm	1.00-2.00 pm	2.00-3.00 pm	3.00-4.00pm
	1	2	3	4	5	6	7
MON	DICA	AP	LICA	LUNCH	IC PDC/DCLAB		
TUE	DC (T)	DC	LICA (T)	LUNCH	CAO	PEHV	
WED	PEHV	IC PDC/DCLAB			LUNCH	LICA	
THU	DICA	CAO (T)	AP	LUNCH	DICA LAB		
FRI	CAO	DC	AP (T)	DICA (T)	LUNCH		
SAT	LICA	AP	CAO	LUNCH	DICA	DC	PEHV (T)
CAO	Computer Architecture & Organization				Mr.Y.PAVAN KUMAR		
DC	Digital Communications				Mr.B SUDHIR		
AWP	Antennas and Wave Propagation				Mr JOHN SAIDA SHAIK		
LICA	Linear IC Applications				Dr.D.B V N S M NAGESH		
DICA & DSD	Digital System Design & Digital IC Applications				Dr.P SUNITHA		
PEHV	Professional Ethics and Human Values				Mrs.P.SRAVANI		
TUTORIAL HOURS	CAO				Mr.Y.PAVAN KUMAR		
	DC				Mr.B SUDHIR		
	AWP				Mr JOHN SAIDA SHAIK		
	LICA				Dr.D.B V N S M NAGESH		
	DICA & DSD				Dr.P SUNITHA		
DC LAB	Digital Communications Lab				Mr K PASIPALANA RAO Mrs ASAMRUTHAVARSHINI Ms.CH.N.S.T.DEVI		
DICA Lab	Digital System Design & DICA Lab				Mrs.P SUNITHA Mr.V.PRASANTH Ms.G. VIRANYA Ms.P.ANURADHA		
IC-PDC LAB	Integrated Circuits & Pulse & Digital Circuits Lab				Mr.B.KALYANCHAKRAVARTHY Mr.R.V.SIMHADRI Mrs K DEEPTHI		



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ACADEMIC YEAR 2020-21				IV ECE -'A' I SEM			
Days	01.00-2.00 PM	2.00-3.00 PM	3.00-4.00 PM	4.00-5.00 PM	5.00-6.00 PM	6.00-7.00 PM	7.00-8.00 PM
	1	2	3	4	5	6	7
MON	CN (T)	MWE	MWE	BREAK	MWE/DSP LAB		
TUE	RE	RE(T)	ES	MWE	BREAK	OC	
WED	ES	ES	DIP (T)	MWE (T)	BREAK	CN	
THU	RE	MWE/DSP LAB			BREAK	DIP	
FRI	OC	OC (T)	RE	ES (T)	BREAK	DIP	
SAT	CN	CN	DIP	OC	BREAK		
DIP	Digital Image Processing				Dr.AKULA SRINIVAS		
OC	Optical Communication				Mrs K SURYA KUMARI		
MWE	Microwave Engineering				Dr.U SATHEESHWARAN		
RE	Radar Engineering				Mr M SRINIVAS		
ES	Embedded Systems				Mrs G V UJWALA		
CN	Computer Networks				Mr CH SATISH		
TUTORIAL HOURS	DIP				Dr.AKULA SRINIVAS		
	OC				Mrs K SURYA KUMARI		
	MWE				Dr.U SATHEESHWARAN		
	RE				Mr M SRINIVAS		
	ES				Mrs G V UJWALA		
CN				Mr CH SATISH			
MWE Lab	Microwave Engineering Lab				Mrs G V UJWALA Mr.K.RAMBABU Mr.CH LAKSHMINARAYANA		
DSP Lab	Digital Signal Processing Lab				Dr.AKULA SRINIVAS Mr V.SATTIBABU Mr JOHN SAIDA SHAIK		



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ACADEMIC YEAR 2020-21				IV ECE '-B' I SEM			
Days	09-00 - 10.00 am.	10.00 - 11.00 am.	11.00 - 12.00 pm.	12.00-1.00 pm	1.00-2.00 pm	2.00-3.00 pm	3.00-4.00pm
	1	2	3	4	5	6	7
MON	RE (T)	DIP (T)	RE	MWE	LUNCH	ES	
TUE	MWE	MWE (T)	ES (T)	LUNCH	MWE/DSP LAB		
WED	ES	OC (T)	OC	DIP	LUNCH	ES	
THU	CN	CN (T)	DIP	RE	LUNCH		
FRI	OC	MWE/DSP LAB			LUNCH	OC	
SAT	CN	CN	DIP	RE	LUNCH	MWE	
DIP	Digital Image Processing				Mrs B VASANTHA LAKSHMI		
OC	Optical Communication				Mrs G SRILAKSHMI		
MWE	Microwave Engineering				Dr..D NATARAJ		
RE	Radar Engineering				Mr M.BRAHMARAJU		
ES	Embedded Systems				Mr K PASIPALANA RAO		
CN	Computer Networks				Ms.G. VIRANYA		
TUTORIAL HOURS	DIP				Mrs B VASANTHA LAKSHMI		
	OC				Mrs G SRILAKSHMI		
	MWE				Dr..D NATARAJ		
	RE				Mr M.BRAHMARAJU		
	ES				Mr K PASIPALANA RAO		
	CN				Ms.G. VIRANYA		
MWE Lab	Microwave Engineering Lab				Dr..D NATARAJ Mr.K.RAMBABU Mr.B.MANIBABU		
DSP Lab	Digital Signal Processing Lab				Mr M SRINIVAS Mrs.B.SURYA SUDHA RANI Mr V.SATTIBABU		



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ACADEMIC YEAR 2020-21		IV ECE -'C' I SEM					
Days	09-00 - 10.00 am.	10.00 - 11.00 am.	11.00 - 12.00 pm.	12.00- 1.00 pm	1.00- 2.00 pm	2.00- 3.00 pm	3.00-4.00pm
	1	2	3	4	5	6	7
MON	RE	RE	ES	CN	LUNCH	OC (T)	
TUE	CN (T)	MWE	MWE	DIP (T)	LUNCH	DIP	
WED	OC	MWE/DSP LAB			LUNCH	RE (T)	
THU	OC	CN	CN	ES	LUNCH	OC	
FRI	DIP	DIP	RE	LUNCH	MWE/DSP LAB		
SAT	ES (T)	OC	MWE (T)	MWE	LUNCH		
DIP	Digital Image Processing				Mr.V SATTI BABU		
OC	Optical Communication				Mr.R.V.SIMHADRI		
MWE	Microwave Engineering				Mr.CH LAKSHMINARAYANA		
RE	Radar Engineering				Mr.P VENKATESH		
ES	Embedded Systems				Mrs G V UJWALA		
CN	Computer Networks				Mr T.DEEPAK		
TUTORIAL HOURS	DIP				Mr.V SATTI BABU		
	OC				Mr.R.V.SIMHADRI		
	MWE				Mr.CH LAKSHMINARAYANA		
	RE				Mr.P VENKATESH		
	ES				Mrs G V UJWALA		
	CN				Mr T.DEEPAK		
MWE Lab	Microwave Engineering Lab				Mrs G V UJWALA Dr.U SATHEESHWARAN Mr.K.RAMBABU		
DSP Lab	Digital Signal Processing Lab				Dr.AKULA SRINIVAS Mr T.DEEPAK Mrs.B.SURYA SUDHA RANI		



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ACADEMIC YEAR 2020-21 IV ECE -'D' I SEM							
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	1	2	3	4	5	6	7
MON	CN	CN	OC (T)	DIP (T)	LUNCH	DIP	
TUE	ES	MWE/DSP LAB			LUNCH	ES (T)	
WED	DIP	MWE	MWE (T)	CN (T)	LUNCH	OC	
THU	ES	ES	OC	LUNCH	MWE/DSP LAB		
FRI	OC	RE	MWE	MWE	LUNCH	RE (T)	
SAT	RE	RE	ES	CN	LUNCH		
DIP	Digital Image Processing				Mrs B VASANTHA LAKSHMI		
OC	Optical Communication				Mrs G SRILAKSHMI		
MWE	Microwave Engineering				Dr..D NATARAJ		
RE	Radar Engineering				Mr M.BRAHMARAJU		
ES	Embedded Systems				Mr K PASIPALANA RAO		
CN	Computer Networks				Ms.P ANURADHA		
TUTORIAL HOURS	DIP				Mrs B VASANTHA LAKSHMI		
	OC				Mr K PASIPALANA RAO		
	MWE				Dr..D NATARAJ		
	RE				Mr M.BRAHMARAJU		
	ES				Mrs G SRILAKSHMI		
	CN				Ms.P ANURADHA		
MWE Lab	Microwave Engineering Lab				Dr..D NATARAJ Mr.B.MANIBABU Ms.G. VIRANYA		
DSP Lab	Digital Signal Processing Lab				Dr.AKULA SRINIVAS Mr JOHN SAIDA SHAIK Mrs.B.SURYA SUDHA RANI		



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(Approved by AICTE, Permanently Affiliated to JNTUK, KAKINADA)

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ACADEMIC YEAR 2020-21 IV ECE -'E' I SEM							
Days	09-00 - 10.00 am.	10.00 - 11.00 am.	11.00 - 12.00 pm.	12.00- 1.00 pm	1.00-2.00 pm	2.00-3.00 pm	3.00- 4.00pm
	1	2	3	4	5	6	7
MON	RE	MWE/DSP LAB			LUNCH	CN	
TUE	ES	DIP	DIP	OC	LUNCH	RE	
WED	DIP	OC	MWE	LUNCH	MWE/DSP LAB		
THU	MWE	MWE	ES (T)	OC (T)	LUNCH	OC	
FRI	DIP (T)	CN (T)		MWE (T)			
SAT	RE	RE (T)	CN	ES	LUNCH	CN	
DIP	Digital Image Processing				Dr.AKULA SRINIVAS		
OC	Optical Communication				Mrs K SURYA KUMARI		
MWE	Microwave Engineering				Mr.CH LAKSHMINARAYANA		
RE	Radar Engineering				Mr M SRINIVAS		
ES	Embedded Systems				Mrs A S AMRUTHAVARSHINI		
CN	Computer Networks				Mr CH SATISH		
TUTORIAL HOURS	DIP				Dr.AKULA SRINIVAS		
	OC				Mrs K SURYA KUMARI		
	MWE				Mr.CH LAKSHMINARAYANA		
	RE				Mr M SRINIVAS		
	ES				Mrs A S AMRUTHAVARSHINI		
	CN				Mr CH SATISH		
MWE Lab	Microwave Engineering Lab				Mr.CH LAKSHMINARAYANA Dr.U SATHEESHWARAN Mr.B.MANIBABU		
DSP Lab	Digital Signal Processing Lab				Mr M SRINIVAS Mr T.DEEPAK Mrs.B.SURYA SUDHA RANI		