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PRAGATIENGINEERINGCOLLEGE::SURAMPALEM

(Autonomous)

EXAMINATION SECTION

M. TECH II SEMESTER (R16) SUPPLEMENTARY EXAMINATIONS-September -2022

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TIME TABLE

Time: 10.00A.M to 01.00 P.M

Specialization	20-09-2022 (Tuesday)	22-09-2022 (Thursday)	24-09-2022 (Saturday)	26-09-2022 (Monday)	28-09-2022 (Wednesday)	30-09-2022 (Friday)
CAD/CAM	Simulation Modeling of Manufacturing Systems (16062T05)	Optimization and Reliability (16062T06)	Computer Graphics (16062T07)	Finite Element Methods (16062T08)	Elective-III Concurrent Engineering (16062D09)	Elective-IV Intelligent Manufacturi ng Systems (16062D12)
Power Electronics and Electric Drives (PE&ED)	Switched Mode Power Conversion (16052T05)	Electric Drives-II (16052T06)	Digital Controllers (16052T07)	Custom Power devices (16052T08)	Elective–III Electrical Distribution Systems (16052D09)	Elective-IV Special Machines (16052D11)
Embedded Systems (ES)	Hardware Software Co-Design (16022T05)	DSP Integrated Circuits (16022T06)	Embedded Networking (16022T07)	CPLD and FPGA Architectures and Applications (16022T08)	Elective-III Sensors and Actuators (16022D07)	Elective-IV System on Chip Design (16022D10)

R16



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Low Power VLSI Design (16042T05)	CMOS Mixed Signal Circuit Design (16042T06)	Reconfigurable Architectures For VLSI (16042T07)	Design For Testability (16042T08)	Elective-III Digital Signal Processors & Architectures (16042D08)	Elective-IV Optimization Techniques in VLSI Design (16042D11)	
Cyber Security (16012T07)	Computer Networks (16012T08)	Big Data Analytics (16012T09)	Elective-I Human Computer Interaction (16012D03)	Elective-II Mobile Computing (16012D05)	Elective-III 1)Cloud Computing (16012D08) 2)Software Engineering (16012D09)	
	Low Power VLSI Design (16042T05)	(Tuesday) (Thursday) Low Power VLS1 CMOS Mixed Signal Design Circuit Design (16042T05) (16042T06)	(Tuesday)(Thursday)(Saturday)Low Power VLSI Design (16042T05)CMOS Mixed Signal Circuit Design (16042T06)Reconfigurable Architectures For VLSI (16042T07)Cyber Security Cyber SecurityComputer NetworksBig Data Analytics	(Tuesday)Int of Abec24-09-202226-09-2022(Thursday)(Thursday)(Saturday)(Monday)Low Power VLSI Design (16042T05)CMOS Mixed Signal Circuit Design (16042T06)Reconfigurable Architectures For VLSI (16042T07)Design For Testability (16042T08)Cyber Security (16012T07)Computer Networks (16012T08)Big Data Analytics (16012T09)Elective-I Human Computer Interaction	20-09-2022 (Tuesday)22-09-2022 (Thursday)24-09-2022 (Saturday)26-09-2022 (Monday)28-09-2022 (Wednesday)Low Power VLSI Design (16042T05)CMOS Mixed Signal Circuit Design (16042T06)Reconfigurable Architectures For VLSI (16042T07)Design For Testability (16042T08)Elective-IIICyber Security (16012T07)Computer Networks (16012T08)Big Data Analytics (16012T09)Elective-I Human Computer InteractionElective-II Mobile Computing (16012T05)	

Station : Surampalem Date : 09-09-2022. م نبانی م

Chief Controller of Examinations

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