

K.SURYAKUMARI

D.NO.:9-107,
JAYAPRAKASH NAGAR,
TURANGI
KAKINADA,533016

Email:kumarisep19@gmail.com
Phone:9666097654

OBJECTIVE:

- To work as a Professor which offers me to show,learn and upgrade my skills,ability that work towards the growth of the College.

EDUCATIONAL QUALIFICATION:

- M.Tech (VLSI) from ADITYA ENGINEERING COLLEGE , SURAMPALEM in 2011with First class with Distinction.
- Ph.D(Communication in VLSI) pursuing in ACHARYA NAGARJUNA UNIVERSITY at GUNTUR

EXPERIENCE SUMMARY:

- 2007 – till date working as Asst.Professor in ECE Department at Pragati Engineering College,Surampalem.

SUBJECTS DEALT:

- | | |
|--|-----------|
| • Principles of communications | (1 Time) |
| • Sattelite communications | (1 Time) |
| • Pulse and digital communication | (2 Times) |
| • Electronics measurements and instruments | (1Time) |
| • Digital logic design | (2 Times) |
| • Digital communications | (4 Times) |
| • Analog communications | (1 Time) |
| • Switching Theory & Logic Design | (2 Time) |
| • Digital signal processing | (1 Time) |
| • VLSI | (1 Time) |
| • LDIC | (1 Time) |

LABS HANDLED:

- DC Lab
- AC lab
- PDC Lab
- EDC Lab
- LICA Lab
- ECAD Lab

MAJOR PROJECTS DONE UNDER MY GUIDANCE:

- GAS DETECTION using ZIG Bee
- GAS HAZARDOUS using RF ID
- DETECTION OF TRAFIC DENSITY AND CONTROL using HDL
- IMPLEMENTATION OF BLACK BOX..

CONFERENCES AND WORKSHOPS ATTENDED:

- Participated in one day workshop on “Research Methodologies” on 24th November 2009 at JNTUCEK, Kakinada.
- Participated in two day workshop on “VLSI Design Software” During 27th& 28th September 2008 at JNTUCEK, Kakinada.
- Attended one day workshop on “COMPETENCY BASED CURRICULUM DESIGN” conducted on 8th April, 2007 at JNTUCEK, Kakinada.
- Participated in two day state level workshop on “CURRENT PRACTICES IN ELECTRONIC COMMUNICATION SYSTEMS” conducted on 26th and 27th February 2005 by Pragati Engineering College, Surampalem.
- Participated in WOLTEC-05 organized by MVGR College of Engineering, vizianagaram.
- Attended faculty development programme on “ MOBILE COMPUTING AND INFORMATION SECURITY “ Conducted by TCS Ltd. At Pragati Engineering College, Surampalem.

PAPERS PUBLISHED:

- An efficient memory architecture for network intrusion detection system using pattern partitioning and parallel string matching in International Journal (IJCER)on 2013 January.
- Implementation of uart with data encryption using FPGA in ICEECE on 18 may.
- Physical design of uart with AES based on FPGA in IEEECE at Hyderabad
- Implementation of efficient gate level modified SRCOA USING HDL in IJSEAT
A static time analysis of 1 bit to 32 pageSCA architecture for logic test

PERSONAL SKILLS:

- Ability to impress students with my teaching.
- Ability to Perfectly complete allocated work with in time as per schedule.
- Ability to do any concern work given by management

ADDITIONAL DUTIES:

- Worked as Lab – In charge for AC/DC Lab
- Worked as library in-charge of ECE department.
- Working as faculty in-charge of COURSE FILES.
- Worked as CLASS TEACHER.
- Worked as an active participation in NBA time(library incharge and AC/DC lab Incharge).

PERSONAL DETAILS:

- Husband's Name : G.David Raju
- Date of Birth : 15-06-1986
- Languages Known : Telugu,English,.
- Strengths : friendly natureand Positive thinking .

Place:

Date:

(G.SURYAKUMARI)