

**PRAGATIENGINEERINGCOLLEGE::SURAMPALEM**  
(Autonomous)

**EXAMINATION SECTION**

***M.TECH II SEMESTER REGULAR AND SUPPLEMENTARY EXAMINATIONS(R16)–JUNE-2019***

**TIME TABLE**

**Time: 10.00A.M to 01.00 P.M**

Specialization	14-06-2019 (Friday)	17-06-2019 (Monday)	19-06-2019 (Wednesday)	21-06-2019 (Friday)	24-06-2019 (Monday)	26-06-2019 (Wednesday)
<b>CAD/CAM</b>	Simulation Modeling of Manufacturing Systems ✓ (16062T05)	Optimization and Reliability ✓ (16062T06)	Computer Graphics ✓ (16062T07)	Advanced Finite Element Method ✓ (16062T08)	<b>Elective–III</b> Concurrent Engineering ✓ (16062D09)	<b>Elective–IV</b> Intelligent Manufacturing Systems ✓ (16062D12)
<b>Power Electronics and Electric Drives (PE&amp;ED)</b>	Switched Mode Power Conversion ✓ (16052T05)	Electric Drives-II ✓ (16052T06)	Digital Controllers ✓ (16052T07)	Custom Power devices ✓ (16052T08)	<b>Elective–III</b> ✓ Electrical Distribution Systems ✓ (16052D09)	<b>Elective–IV</b> ✓ Special Machines ✓ (16052D11)
<b>Embedded Systems (ES)</b>	Hardware Software Co-Design ✓ (16022T05)	DSP Integrated Circuits ✓ (16022T06)	Embedded Networking ✓ (16022T07)	CPLD and FPGA Architectures and Applications ✓ (16022T08)	<b>Elective–III</b> ✓ Sensors and Actuators ✓ (16022D07)	<b>Elective–IV</b> ✓ System on Chip Design ✓ (16022D10)

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Specialization	14-06-2019 (Friday)	17-06-2019 (Monday)	19-06-2019 (Wednesday)	21-06-2019 (Friday)	24-06-2019 (Monday)	26-06-2019 (Wednesday)
<b>VLSI System Design</b> ✓ (VLSISD)	Low Power VLSI Design ✓ (16042T05)	CMOS Mixed Signal Circuit Design ✓ (16042T06)	Reconfigurable Architectures For VLSI ✓ (16042T07)	Design For Testability ✓ (16042T08)	<b>Elective-III</b> Digital Signal Processors & Architectures ✓ (16042D08)	<b>Elective-IV</b> Optimization Techniques in VLSI Design ✓ (16042D11)
<b>Computer Science and Engineering</b> ✓ (CSE)	Cyber Security ✓ (16012T07)	Computer Networks ✓ (16012T08)	Big Data Analytics ✓ (16012T09)	<b>Elective-I</b> ✓ Human Computer Interaction ✓ (16012D03)	<b>Elective-II</b> ✓ Mobile Computing ✓ (16012D05)	<b>Elective-III</b> ✓ Software Engineering ✓ (16012D09)

Station : Surampalem

Date : 31-05-2019

*Sprandy*  
Chief Controller of Examinations

*J. N. Sankar*