

**PRAGATI ENGINEERING COLLEGE (Autonomous)**  
1-378, ADB Road, Surampalem, near Peddapuram  
**M.Tech I year II semester**

**II Mid Examination Time-table**

Academic year 2018-19

Time: 10.00A.M to 12.00A.M

BRANCHES AND SPECIALIZATIONS	03-06-2019	04-06-2019	06-06-2019	07-06-2019	08-06-2019	10-06-2019
	(Monday)	(Tuesday)	(Thursday)	(Friday)	(Saturday)	(Monday)
EEE Power Electronics and Electric Drives	Switched Mode Power Conversion	Electric Devices-II	Digital Controllers	Custom Power Devices	<b>ELECTIVE-III</b> Electrical Distribution Systems	<b>ELECTIVE-IV</b> Special Machines
MECHANICAL CAD/CAM	Simulation Modeling of Manufacturing Systems	Optimization and Reliability	Computer Graphics	Advanced Finite Element Methods	<b>ELECTIVE-III</b> Concurrent Engineering	<b>ELECTIVE-IV</b> Intelligent Manufacturing Systems
CSE Computer Science and Engineering	Cyber Security	Computer Networks	Big Data Analytics	<b>ELECTIVE-I</b> Human Computer Interaction	<b>ELECTIVE-II</b> Mobile computing	<b>ELECTIVE-III</b> Software Engineering
ECE VLSI System Design	Low Power VLSI Design	CMOS Mixed Signal Circuit Design	Reconfigurable Architectures for VLSI	Design for Testability	<b>Elective-III</b> Digital Signal Processors & Architectures	<b>Elective-IV</b> Optimization Techniques in VLSI Design
ECE Embedded Systems	Hardware software Co-Design	DSP Integrated Circuits	Embedded Networking	CPLD and FPGA Architectures and Applications	<b>Elective-III</b> Sensors and Actuators	<b>Elective-IV</b> System on Chip Design

Station : Surampalem  
Date: 24-05-2019

Chief Controller of Examinations

8.

*J. Sahu*

*Sprandy*