

**PRAGATI ENGINEERING COLLEGE::SURAMPALEM**  
(Autonomous)

**EXAMINATION SECTION**

***M.TECH I SEMESTER REGULAR and SUPPLEMENTARY END EXAMINATIONS (R16) –JANUARY-2019***

**TIME TABLE**

| Time: 10.00 A.M to 01.00 P.M                                             |                                                         |                                                                 |                                                       |                                                         |                                                           |                                                                            |
|--------------------------------------------------------------------------|---------------------------------------------------------|-----------------------------------------------------------------|-------------------------------------------------------|---------------------------------------------------------|-----------------------------------------------------------|----------------------------------------------------------------------------|
| Specialization                                                           | 21-01-2019<br>(Monday)                                  | 23-01-2019<br>(Wednesday)                                       | 25-01-2019<br>(Friday)                                | 28-01-2019<br>(Monday)                                  | 30-01-2019<br>(Wednesday)                                 | 01-02-2019<br>(Friday)                                                     |
| <b>CAD/CAM</b>                                                           | Industrial Robotics<br>(16061T01)                       | Computer Aided<br>Manufacturing<br>(16061T02)                   | Special Manufacturing<br>Processes<br>(16061T03)      | Geometric Modeling<br>(16061T04)                        | Elective –I<br><br>Nano Technology<br>(16061D03)          | Elective –II<br><br>Computer Aided<br>Process Planning<br>(16061D06)       |
| <b>Power<br/>Electronics<br/>and Electric<br/>Drives<br/>(PE&amp;ED)</b> | Electrical Machine<br>Modeling & Analysis<br>(16051T01) | Analysis of Power<br>Electronic Converters<br>(16051T02)        | Electric Drives – I<br>(16051T03)                     | Flexible AC<br>Transmission Systems<br>(16051T04)       | Elective –I<br><br>Power Quality<br>(16051D02)            | Elective –II<br><br>Artificial<br>Intelligence<br>Techniques<br>(16051D05) |
| <b>Embedded<br/>Systems<br/>(ES)</b>                                     | Embedded System<br>Design<br>(16021T01)                 | Microcontrollers for<br>Embedded System<br>Design<br>(16021T02) | Embedded Real Time<br>Operating Systems<br>(16021T03) | Signal Processing for<br>Embedded Systems<br>(16021T04) | Elective –I<br><br>Digital System<br>Design<br>(16021D01) | Elective –II<br><br>Embedded Control<br>Systems<br>(16021D05)              |



| Specialization                                  | 21-01-2019<br>(Monday)                                              | 23-01-2019<br>(Wednesday)                                        | 25-01-2019<br>(Friday)                                           | 28-01-2019<br>(Monday)                       | 30-01-2019<br>(Wednesday)                                 | 01-02-2019<br>(Friday)                                    |
|-------------------------------------------------|---------------------------------------------------------------------|------------------------------------------------------------------|------------------------------------------------------------------|----------------------------------------------|-----------------------------------------------------------|-----------------------------------------------------------|
| VLSI SD                                         | VLSI Design<br>Automation<br>(16041T01)                             | CMOS Analog IC<br>Design<br>(16041T02)                           | CPLD and FPGA<br>Architectures and<br>Applications<br>(16041T03) | CMOS Digital IC<br>Design<br>(16041T04)      | Elective -I<br>Soft Computing<br>Techniques<br>(16041D03) | Elective -II<br>Digital Design<br>using HDL<br>(16041D04) |
| Computer<br>Science and<br>Engineering<br>(CSE) | Advanced Data<br>Structures and<br>Algorithm Analysis<br>(16011T01) | Mathematical<br>Foundations of<br>Computer Science<br>(16011T02) | Computer Organization<br>and Architecture<br>(16011T03)          | Database Management<br>Systems<br>(16011T04) | Operating Systems<br>(16011T05)                           | Data Warehousing<br>and Data Mining<br>(16011T06)         |

Station : Surampalem  
Date : 07-01-2019

verified by  
[Signature]  
[Date]

[Signature]  
Chief Controller of Examinations

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