



PRAGATI ENGINEERING COLLEGE (Autonomous)

Approved by AICTE, Permanently Affiliated to JNTUK & Accredited by NAAC with 'A' Grade
1-378, ADB Road, Surampalem, East Godavari District, Andhra Pradesh

RESULTS

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Branch: VLSI SYSTEM DESIGN

Course/Sem: M.TECH II SEMESTER Regular Examinations (R16)

Held in: JUNE 2018

Roll No SGPA CGPA

VLSI SD	VLSI SD	VLSI SD	VLSI SD	VLSI SD	VLSI SD	VLSI SD
Low Power VLSI Design	CMOS Mixed Signal Circuit Design	Reconfigurable ArchitecturesFor VLSI	Design For Testability	Digital Signal Processors & Architectures	Optimization Techniques in VLSI Design	VLSI Laboratory-II
Grade	Grade	Grade	Grade	Grade	Grade	Grade
S	S	B	B	A	A	O
A	A	A	A	A	S	O
S	S	A	S	S	S	O
B	A	B	C	B	B	O
S	S	B	C	F	B	O

Note: In case of any deviation, the concern students may contact the COE immediately

Letter Grade Indicates Grade Points (O - 10.00, S - 9.00, A - 8.00, B - 7.00, C - 6.00, F - 0.)

Principal