

**PRAGATIENGINEERINGCOLLEGE::SURAMPALEM**  
**(Autonomous)**

**EXAMINATION SECTION**

***M.TECH II SEMESTER REGULAR AND SUPPLEMENTARY EXAMINATIONS(R16)–JUNE-2018***

**TIME TABLE**

**Time: 10.00A.M to 01.00 P.M**

<b>Specialization</b>	<b>22-06-2018 (Friday)</b>	<b>25-06-2018 (Monday)</b>	<b>27-06-2018 (Wednesday)</b>	<b>29-06-2018 (Friday)</b>	<b>02-07-2018 (Monday)</b>	<b>04-07-2018 (Wednesday)</b>
<b>CAD/CAM</b>	Simulation Modeling of Manufacturing Systems (16062T05)	Optimization and Reliability (16062T06)	Computer Graphics (16062T07)	Advanced Finite Element Method (16062T08)	<b>Elective–III</b> Concurrent Engineering (16062D09)	<b>Elective–IV</b> Intelligent Manufacturing Systems (16062D12)
<b>Power Electronics and Electric Drives (PE&amp;ED)</b>	Switched Mode Power Conversion (16052T05)	Electric Drives-II (16052T06)	Digital Controllers (16052T07)	Custom Power devices (16052T08)	<b>Elective–III</b> Electrical Distribution Systems (16052D09)	<b>Elective–IV</b> Special Machines (16052D11)
<b>Embedded Systems (ES)</b>	Hardware Software Co-Design (16022T05)	DSP Integrated Circuits (16022T06)	Embedded Networking (16022T07)	CPLD and FPGA Architectures and Applications (16022T08)	<b>Elective–III</b> Sensors and Actuators (16022D07)	<b>Elective–IV</b> System on Chip Design (16022D10)

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<b>VLSI System Design (VLSISD)</b>	Low Power VLSI Design (16042T05)	CMOS Mixed Signal Circuit Design (16042T06)	Reconfigurable Architectures For VLSI (16042T07)	Design For Testability (16042T08)	<b>Elective-III</b>  Digital Signal Processors & Architectures (16042D08)	<b>Elective-IV</b>  Optimization Techniques in VLSI Design (16042D11)
<b>Computer Science and Engineering (CSE)</b>	Cyber Security (16012T07)	Computer Networks (16012T08)	Big Data Analytics (16012T09)	<b>Elective-I</b>  Human Computer Interaction (16012D03)	<b>Elective-II</b>  Mobile Computing (16012D05)	<b>Elective-III</b>  Software Engineering (16012D09)



**Chief Controller of Examinations**

Station : Surampalem

Date : 03-06-2018