



# PRAGATI ENGINEERING COLLEGE (Autonomous)

Approved by AICTE, Permanently Affiliated to JNTUK & Accredited by NAAC with 'A' Grade  
1-378, ADB Road, Surampalem, East Godavari District, Andhra Pradesh

## RESULTS

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Branch: VLSI SYSTEM DESIGN

Course/Sem: M.TECH I SEMESTER Regular Examinations (R16)

Held in: JANUARY 2018

Roll No      SGPA      CGPA

			VLSI SD						
			CMOS Analog IC Design	CMOS Digital IC Design	CPLD and FPGA Architectures and Applications	Digital Design using HDL	Soft Computing Techniques	VLSI Design Automation	VLSI Laboratory-I
			Grade	Grade	Grade	Grade	Grade	Grade	Grade
17A31D5701	7.90	7.90	S	A	B	A	B	B	O
17A31D5702	7.30	7.30	B	B	B	A	C	B	O
17A31D5703	8.65	8.65	S	A	A	S	A	S	O
17A31D5704	7.15	7.15	B	B	B	B	C	B	O
17A31D5705	7.90	7.90	A	B	B	B	A	S	O

Note: In case of any deviation, the concern students may contact the COE immediately

Letter Grade Indicates Grade Points (O - 10.00, S - 9.00, A - 8.00, B - 7.00, C - 6.00, F - 0.)

Principal