

PRAGATI ENGINEERING COLLEGE (Autonomous)

Approved by AICTE, Permanently Affiliated to JNTUK& Accredited by NAAC with 'A' Grade 1-378, ADB Road, Surampalem, East Godavari District, Andhra Pradesh

RESULTS

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Branch: VLSI SYSTEM DESIGN COURSe/Sem: M.TECH I SEMESTER Supplementary Examinations (R16)

Held in: JANUARY 2018

Roll No	SGPA	A CGPA			
			VLSI SD		
			CMOS Digital IC Design	CPLD and FPGA Architectures and Applications	Soft Computing Techniques
			Grade	Grade	Grade
16A31D5702	6.85	6.85	С	В	
16A31D5705					F