



PRAGATI ENGINEERING COLLEGE (Autonomous)

Approved by AICTE, Permanently Affiliated to JNTUK & Accredited by NAAC with 'A' Grade
1-378, ADB Road, Surampalem, East Godavari District, Andhra Pradesh

RESULTS

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Branch: VLSI SYSTEM DESIGN

Course/Sem: M.TECH I SEMESTER Regular Examinations (R16)

Held in: DECEMBER 2016

Roll No SGPA CGPA

Roll No	SGPA	CGPA	VLSI SD	VLSI SD	VLSI SD	VLSI SD	VLSI SD	VLSI SD	VLSI SD
			VLSI Design Automation	CMOS Analog IC Design	CPLD and FPGA Architectures and Applications	CMOS Digital IC Design	Soft Computing Techniques	Digital Design using HDL	VLSI Laboratory-I
			Grade	Grade	Grade	Grade	Grade	Grade	Grade
16A31D5701	7.15	7.15	A	A	B	C	C	C	O
16A31D5702			B	B	F	F	C	F	O
16A31D5703			A	A	A	B	B	F	O
16A31D5704	7.15	7.15	A	A	B	C	C	C	O
16A31D5705			B	A	B	B	F	F	O

Note: In case of any discrepancy, the concerned student may contact the CE within 3 days from the date of publication of result

Controller of Examinations

Principal